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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : DIRK HOTTGENROTH  
Filed : CONCURRENTLY HEREWITH  
Title : METHOD FOR OPERATING AN INTEGRATED MEMORY UNIT

INFORMATION DISCLOSURE STATEMENT

Hon. Commissioner of Patents and Trademarks,  
Washington, D.C. 20231

Sir:

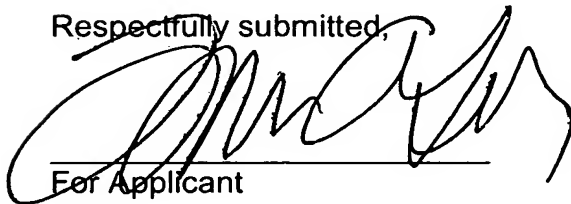
In accordance with 37 C.F.R. 1.98 copies of the following patents and/or publications are submitted herewith:

U.S. Patent 5,327,390 (Takasugi), dated July 5, 1994;

U.S. Patent 5,801,554 (Momma et al.), dated September 1, 1998;

Reese, E. et al.: "A 4K x 8 Dynamic RAM with Self-Refresh", IEEE, Vol. SC-16, No. 5, October 1981, pp. 479-487.

Respectfully submitted,



For Applicant

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